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L6: Entry 2 of 2

File: DWPI

Nov 30, 1999

DERWENT-ACC-NO: 2000-079608
DERWENT-WEEK: 200007
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TITLE: Digital signal processing method of digital information transmitted or received by portable telephone - involves reading signals selectively bit-by-bit and performing calculations parallelly for every channels based on specific formula using EX-OR gates

PATENT-ASSIGNEE:

ASSIGNEE

CODE

TOYO COMMUNICATION EQUIP CO

TOCM

PRIORITY-DATA: 1998JP-0145044 (May 11, 1998)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
JP 11327939 A	November 30, 1999		006	G06F011/10

APPLICATION-DATA:

PUB-NO	APPL-DATE	APPL-NO	DESCRIPTOR
JP 11327939A	May 11, 1998	1998JP-0145044	

INT-CL (IPC): G06 F 11/10; G10 L 9/00; H03 M 13/00; H04 J 3/00; H04 L 1/00

ABSTRACTED-PUB-NO: JP 11327939A

BASIC-ABSTRACT:

NOVELTY - The digital signal of every channel are assigned to each longitudinal addresses and stored in memory (FF1-FF9). The signals are read bit-by-bit selectively from the respective addresses and the calculation of cyclic redundancy codes using EX-OR gates (12-20) is done parallelly for all signals on the basis of specific formula.

USE - For calculating cyclic redundancy codes in all digital signals transmitted or received by portable telephone.

ADVANTAGE - Velocity of calculation is accelerated since calculation is performed parallelly and it is a short time process involving low cost. DESCRIPTION OF

DRAWING(S) - The figure shows the diagram explaining the calculation apparatus of cyclic redundancy codes. (12-20) EX-OR gates; (FF1-FF9) Memory.

CHOSEN-DRAWING: Dwg.1/8

TITLE-TERMS: DIGITAL SIGNAL PROCESS METHOD DIGITAL INFORMATION TRANSMIT RECEIVE
PORTABLE TELEPHONE READ SIGNAL SELECT BIT BIT PERFORMANCE CALCULATE PARALLEL CHANNEL
BASED SPECIFIC FORMULA GATE

DERWENT-CLASS: P86 T01 U21 W01 W02 W04

EPI-CODES: T01-G01A1; U21-A06; W01-A01; W02-K02; W04-V05;

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